Welcome to EECS 16A!
Designing Information Devices and Systems I

Ana Arias and Miki Lustig
Spring 2022
Instructors

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Office Hours: right after lecture, 11:15-12 Cory 504

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Miki Pandemic Special
Other Staff

Head GSIs:
eecs16a@Berkeley.edu
Email with:
Questions not for Piazza
Conflicts, accommodations for exams etc.
Emergencies
Administrative questions

Course manager
Great resource for 1-1 concerns
Krystle@eecs.Berkeley.edu
Teaching Assistants (TAs) Intro - We are here to help!

- 19 TAs, 35 ASEs/readers!
  - Lots of different research areas and interests represented (by design)
EECS Pathway

• We are here to help!
• We want you to do well in class
  - Have 56 people committed to support you

• If you do well in class — you can get involved!
  - Become an ASE
    ‣ Grade homeworks, assist in labs, tutor and help out in OH, work on improving the notes …
  - Become a uGSI
    ‣ Lab / Discussion / content
  - Become head TA…
Course policies

• Our goal is learning!

• Syllabus is on the course website: https://eecs16a.org/
  - You are responsible for reading and following all course policies listed
  - Almost as long as the US tax code.
    ‣ HW0 is your tax return

• Piazza: http://piazza.com/
  - a resource for you to help each other out

• Gradescope

• Exams via bCourses
Home work

• Due Fridays 11:59pm, on GradeScope
• We have a HW Party! Thu 2-4pm, @Woz
• Office hours — almost every hour of the week

• You grade, we check!
  - Self-grading due Mon 11:59pm
  - Resubmissions due Mon 11:59pm
  - Resub, self-grading due Mon 11:59pm

• Graders verify your self-grading
Class Weekly Events

• Attend lecture — best way to keep along
• Attend discussions (MW)
  - “Free” participation points!
• Lab
  - Required!
  - Attend at your scheduled time!
  - Checkoff during your lab
• Office Hours
  - Faculty after class
  - GSI many
  - HW Party Th 2-4pm @Woz
Learning

• Collaborate and build community on Piazza/HW Party/Study Groups
• Encourage different perspectives – this is Berkeley!
• Everyone here is smart
  - Students have different backgrounds
  - Professors make mistakes – feedback helps
  - If you are struggling, ask for help!

• Optional system to match you into study groups
  - Fill out info in HW0
  - Chance to meet new friends and study mates
Slope is more important than intercept

Day 1  
End of semester

skills (a.u.)

Student 1
Student 2
Student 3

Day 1
End of semester
Slope is more important than intercept

Day 1
End of semester

skills (a.u.)

Student 1
Student 2
Student 3

Day 1
End of semester
End of 2nd semester
We treat all our students with utmost trust and respect, and expect students to return the same trust and respect. In EECS16A we will have zero-tolerance for academic dishonesty. There will be dire consequences for students that violate that trust and the Berkeley code of conduct. Both professors Arias and Lustig are committed to enforcing academic honesty, and dishonesty cases will be punished in their fullest -- no excuses or special circumstances will be considered. Always seek help, never cheat.
Any questions?
Some ideas taught in the class (1)
Some ideas taught in the class (1)
Eigen Values (and vectors)

Used in detection of touch in touch screens

Used in control and Robotics — make autonomous cars run straight!

Used in Ranking of webpages (and other recommendation systems)

Controlling Eigen-Values make optimization problem converge faster (training Deep Neural Nets for example)
Some ideas taught in the class (2)
Some ideas taught in the class (2)
Cross Correlation

Fundamental operation for detection / classification

Used in Radar / Sonar

Used in GPS

Used for cellphone communication

Used in Convolutional Neural Networks
Amount of water a plant receives

Plant growth rate

Some ideas taught in the class (3)

$ax + b$

$ax^2 + bx + c$
Least Squares

Fundamental approach for solving inconsistent sets of linear equations (due to noise and disturbances...)

Used for regression and prediction

Applications in Biology, Social sciences, brain-machine interface, AI
Example application: self-driving cars

- Sense
- Process Data
- Make a model
- Predict
- Actuate i.e take action

External environment

16a

16b
Learning Goals

Not a survey class — rigorous and deep

EECS 16A
• Module 1: Introduction to systems
  - How do we collect data? build a model?
• Module 2: Introduction to circuits and design
  - How do we use a model to solve a problem
• Module 3: Introduction Signal Processing and Machine Learning
  - How do we “learn” models from data, and make predictions?

EECS 16B
• Module 4: Advanced circuit design / analysis
• Module 5: Introduction to control and robotics
• Module 6: Introduction to data analysis and signal processing
16A Lab Examples

**Real World**
- Imaging: EE16A
- Touchscreens:
- Positioning:

**Measurement**
- Measurement circuits

**Processing**
- Systems of linear equations
- Matrix analysis
- Processing circuits
- Cross-correlation Optimization
How did we get from this…

1837
Telegraph

1866
Transatlantic cable

1876
Telephone

2007
The camera phone

Flight of the Conchords S01E03 (HBO)
To this…

Drone delivery

Autonomous cars

Smallest wireless camera

A 1000 student class on a computer screen
Devices as part of a system

- Resistor
- Capacitor
- Inductor
- Diode
- Transistor
Transistor

First transistor - Dec 1947

First integrated circuit 1958
Computational advances due to fabrication advances

**Moore's law** is the observation that the number of transistors in a dense integrated circuit doubles approximately every two years.
FinFET—A Self-Aligned Double-Gate MOSFET Scalable to 20 nm

Dighi Hsinam, Member, IEEE, Wen-Chin Lee, Jakab Kedzierski, Hideki Takeuchi, Kazuya Asano, Member, IEEE, Charles Kuo, Erik Andersson, Tsu-Jae King, Jeffrey Bokor, Fellow, IEEE, and Chenming Hu, Fellow, IEEE

Abstract—MOSFETs with gate length down to 17 nm are reported. To suppress the short channel effect, a novel self-aligned double-gate MOSFET, FinFET, is proposed. By using boron-doped Si₈ₓ₋ₓGeₓ as a gate material, the desired threshold voltage was achieved for the ultrathin body device. The quasi-planar nature of this new variant of the vertical double-gate MOSFETs can be fabricated relatively easily using the conventional planar MOSFET process technologies.

Index Terms—Fully depleted SOI, MOSFET, poly SiGe, short-channel effect.

I. INTRODUCTION

TO DEVELOP sub-50 nm MOSFETs, the double-gate structure has been widely studied. This is because...

source: Chinese Academy of Microelectronics

Go Bears!

source: Intel

Prof. Tsu-Jae King Liu

Prof. Jeff Bokor

Prof. Chenming Hu (left)
Sense of Scale

Miki
172cm
Ana
158cm
Fly
7 mm
Mite
300 um
Blood Cell
7 um
Virus
100 nm
Silicon Atom
0.24 nm

meter | millimeter | micrometer | nanometer

Source: Mark Bohr, IDF14
Completing the puzzle ...

Ada Lovelace wrote the first computer program.

Alan Turing figured out how to build a computer to execute programs.

Claude Shannon Information theorist.